Roll	No.	******************
TIOTT	1 10.	*****************

Total Pages: 3

BSIT/D-14

12256

COMPUTER SYSTEM ARCHITECTURE – I Paper – BSIT-501

Time: Three Hours]

[Maximum Marks:

Regular: 40

Reappear: 45

Note: Question No. 1 is Compulsory. Select four questions in all, one question from each unit.

Compulsory Question 5

- 1. (i) What is the difference between hardwired organization and microprogram organization? (2)
 - (ii) How do you identify whether the instruction is memory reference or register reference or I/O instruction. (2)
 - (iii) What is the difference between micro-operation and macro-operation? (2)
 - (iv) What is the difference between external and internal interrupts. (2)

UNIT-I

2. (i) Explain the common bus system? How the data is manipulated? (5)

THE PERSON AND THE PERSON OF T

(ii) Explain the different memory reference instructions?

(3)

12256/450/KD/23

[P.T.O.

3. (i) Design the block diagram of control unit of computer? Also draw Timing	
GIAW IIIIII MIGHTON	
(11) What are the two instructions needed :-	5)
300 the B hip-hop to 1 ? (2)
UNIT-II	
4. (i) Design an arithmetic circuit with one selection variable	
Thoe two stages.	
$\frac{S}{C_{in}} = 0 \qquad C_{in} = 1$	
D = A + B (add) $D = A + 1 (increment)$	
$1 D = A - 1 (decrement) D' = A + \overline{B} + 1 (subtract)$	
(Subtract)	1
(5)	
(ii) Explain different types of shift micro-operations? (3)	
and explain the bus seed.	
register A to register C. There are 4 registers of 4 bits each.	
(ii) Explain hardware implementation of logic micro- operations?	
(3)	
UNIT-III	
6. (i) Write a note on the following:	
(a) Control memory	
(b) Microinstruction. (2)	1
(c) Microprogram. (1)	
(1)	
12256/450/KD/23 2.	
24EP4024 (8899)	

THE RESIDENCE OF THE PARTY OF T

(ii) The control memory has 4096 words of 24 bits each. (a) How many bits are there in control address register? (b) How many bits are there in each of four input going to multipleness? (1) (c) What are the number of inputs in each multipleness and how many multipleness are needed. Write a note on microprogram sequences? (8) · UNIT-IV Explain various addressing modes with example ? (i) What are the various program control instructions? (ii) A computer has 32 bit instructions and 12 bit addresses. If there are 250 two address instructions, how many one address instructions can be formulated?

12256/450/KD/23